



SRINIVASA EDUCATIONAL SOCIETY'S
PACE INSTITUTE OF TECHNOLOGY & SCIENCES
(AUTONOMOUS)

Approved by AICTE and Govt. of Andhra Pradesh, Accredited by NAAC(A Grade) | Recognized under 2(f) & 12(B) of UGC
Permanently Affiliated to JNTUK, Kakinada, A.P. | ISO 9001:2015, ISO 14001:2015 and ISO 50001:2018 Certified Institution
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Lr No: PACE(A)/ECS/2023-24/EE/167

08/07/2024.


CIRCULAR

All the students are hereby informed that the M.Tech II Semester (R21) Regular (2023 Admitted Batch) and Supplementary (2021,2022 Admitted Batches) examinations will commence from **22/07/2024**. The Time Table for these examinations is given below.

Timing of the Semester End Examination: 02:00 pm to 5:00 pm			
Date	CE (SE)	ECE(VLSI &ES)	CSE (CSE)
22.07.2024 (Mon Day)	Finite Element Method in Structural Engineering	CMOS Analog and Digital IC Design	Data Science Applications with Python
24.07.2024 (Wed Day)	Theory of Plates and Shells	Real Time Operating Systems	Mean Stack Technologies
26.07.2024 (Fri Day)	Advanced Steel Design	Network Security & Cryptography	Internet of Things
29.07.2024 (Mon Day)	Earthquake Resistant Design of Buildings	Low Power VLSI Design	Cloud Computing

NOTE:

- Any Omissions or clashes in this Time Table may please be informed to the Controller of Examinations immediately.
- Even if government declares holiday on any of the above dates, the examinations shall be conducted as usual.


CONTROLLER OF EXAMINATIONS
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Copy to the :

- : CE,ECE,CSE HoD's for circulation to the Staff & Students
- : Coordinator,IQAC- for information
- : Notice board at Exam Cell & System
- : Dean, Academics – for information
- : Office File